L Number	Hits	Search Text	DB	Time stamp
2	171	time adj window same (jitter slew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 13:45
3	37	(define defining adjust adjustable adjusting modify modifying change changing) adj ((time adj window) (time adj slice) (time adj bin)) and (jitter slew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 13:53
4	11	(jitter slew uncertainty) same scan adj chain	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:08
5	12	(calibrate calibration) same phase adj (lock locked locking) same taps	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:12
6	34	(calibrate calibrating calibration) adj (taps (programmable adj taps))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:40
7	3	microprocessor and core adj region and cache adj region and (jitter skew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:46
8	5	microprocessor and cache adj clock with (skew jitter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:42
9	7	microprocessor and cache adj clock and ((skew jitter) with clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:44
10	12	cache adj clock and ((skew jitter) with clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:44
11	3	microprocessor same core adj region same cache adj region	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:47
13	4	microprocessor same core same cache same (jitter skew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:48
14	3	microprocessor same core same cache adj clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 14:48
-	338	(702/57).CCLS.	USPAT; US-PGPUB	2004/07/12 16:48
-	292	(702/66).CCLS.	USPAT; US-PGPUB	2004/07/12 16:52
-	139	(702/69).CCLS.	USPAT; US-PGPUB	2004/07/12 17:20
-	68	(702/71).CCLS.	USPAT; US-PGPUB	2004/07/12 17:22
-	76	(702/72).CCLS.	USPAT; US-PGPUB	2004/07/12 17:30

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-	209	(702/79).CCLS.	USPAT; US-PGPUB	2004/07/12 18:29
-	392	(702/85).CCLS.	USPAT; US-PGPUB	2004/07/12 18:35
-	88	(702/89).CCLS.	USPAT; US-PGPUB	2004/07/12 18:41
-	284	(702/108).CCLS.	USPAT; US-PGPUB	2004/07/12 18:46
-	431	(702/117).CCLS.	USPAT; US-PGPUB	2004/07/12 18:54
-	171	(702/119).CCLS.	USPAT; US-PGPUB	2004/07/12 18:56
-	161	(702/120).CCLS.	USPAT; US-PGPUB	2004/07/12 19:01
-	162	(702/123).CCLS.	USPAT; US-PGPUB	2004/07/12 19:10
-	114	(702/124).CCLS.	USPAT; US-PGPUB	2004/07/12 19:15
-	56	(702/125).CCLS.	USPAT; US-PGPUB	2004/07/12 19:18
-	272	(702/176).CCLS.	USPAT; US-PGPUB	2004/07/12 19:23
-	100	(702/177).CCLS.	USPAT; US-PGPUB	2004/07/12 19:25
-	134	(702/178).CCLS.	USPAT; US-PGPUB	2004/07/12 19:26
-	361	(702/179).CCLS.	USPAT; US-PGPUB	2004/07/12 19:27
-	268	(327/147).CCLS.	USPAT; US-PGPUB	2004/07/13 11:22
-	161	(327/149).CCLS.	USPAT; US-PGPUB	2004/07/13 11:32
-	152	(327/153).CCLS.	USPAT; US-PGPUB	2004/07/13 13:55
-	1105	(327/156).CCLS.	USPAT; US-PGPUB	2004/07/13 14:23
-	614	(327/158).CCLS.	USPAT; US-PGPUB	2004/07/13 15:05
-	434	(327/161).CCLS.	USPAT; US-PGPUB	2004/07/14 13:09
-	153	(375/226).CCLS.	USPAT; US-PGPUB	2004/07/14 13:37
-	173	(375/374).CCLS.	USPAT; US-PGPUB	2004/07/15 13:13
-	299	(375/375).CCLS.	USPAT; US-PGPUB	2004/07/15 13:22
-	709	((375/376).CCLS.) and jitter	USPAT; US-PGPUB	2004/07/15 14:00
-	68	((375/376).CCLS.) and jitter.ti.	USPAT; US-PGPUB	2004/07/15 14:02
-	1	((375/376).CCLS.) and slew.ti.	USPAT; US-PGPUB	2004/07/15 14:03
-	0	((375/376).CCLS.) and uncertainty adj window	USPAT; US-PGPUB	2004/07/15 14:03
-	34	((375/376).CCLS.) and time adj window	USPAT; US-PGPUB	2004/07/15 14:07
-	659	(324/121R).CCLS.	USPAT; US-PGPUB	2004/07/15 18:55
-	54	uncertainty adj window	USPAT; US-PGPUB	2004/07/15 18:56
-	64	uncertainty adj window	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:13
-	11	time adj slice same (jitter slew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:32

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-	5	microprocessor same (target cache) adj clock with jitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:34
-	0	microprocessor same (target cache) adj clock with jskew	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:34
-	3	microprocessor same (target cache) adj clock with skew	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:34
-	10	microprocessor same clock adj domain with (jitter skew)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:35
-	23	microprocessor same clock with (jitter skew) same (window uncertainty)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/15 19:44
-	125	(calibrate calibrating calibration) with (jitter skew uncertainty) near (measurement measuring measure)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 13:21